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A second-order VCO-based Δ ADC using a modified DPLL A. Sanyal and N. Sun This letters presents a novel purely VCO-based architecture for a second-order Δ ADC. The proposed architecture achieves in-band second-order quantization noise shaping. The proposed ADC is highly digital and well suited for design in scaled technologies. The architecture

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Delta-sigma modulation - Wikipedia

This paper presents a second-order NS-SAR ADC employing the error-feedback (EF) structure to realize complex NTF zeros for noise-shaping enhancement with the minimum modification to a standard SAR. It implements a low-power scaling-friendly

EF path by using a passive finite impulse response (FIR) and a comparator-reused dynamic amplifier with process-voltage-temperature (PVT) tracking background calibration.

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Correia A., Barquinha P.M.C., Goes J. (2016) A

Second-Order $\Sigma\Delta$ ADC with Oxide TFTs @ FCT-UNL. In: A Second-Order $\Sigma\Delta$ ADC Using Sputtered IGZO TFTs. SpringerBriefs in Electrical and Computer Engineering.

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Order of $\Sigma\Delta$ ADC 2
Quantization levels 2
(1-bit) Target SNR 100 dB Form CRFB The second step in the design flow is to determine the architectural topology. The toolbox supports a number of popular topologies including: CRFB Cascade-of-resonators, feedback form. CRFF Cascade-of-resonators, feedforward form.

Behavioral Simulation of a Second Order

Discrete Time ...

This books discusses the design, electrical simulation and layout of a 2nd-order $\Sigma\Delta$ analog-to-digital converter (ADC), using oxide thin-film transistors (TFTs) technology. The authors provide a unified view of materials science and electronics engineering, in order to guide readers from both fields through key topics.

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