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1. Navigate to ./software/user/example under the design example directory. 2. Compile the design example application: \$ make 3. Run the test: \$./intel_fpga_pcie_link_test You can run the Intel FPGA IP PCIe link test in manual or automatic mode. — In automatic mode, the application automatically selects the device. The test

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About This Guide This user guide describes the function and operation of the Spartan®-6 FPGA Integrated Endpoint Block for PCI Express® core, including how to design, customize, and implement the core. Guide Contents This manual contains these chapters and appendices: † Chapter 1, Introduction, describes the core and related information, including

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